



- ★ High-speed switching
- ★ Green Device Available
- ★ ESD Protected Embedded

## Product Summary



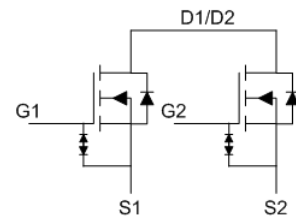
BVDSS	RDSON	ID
20V	9.5mΩ	12A

## Description

The FKBE2738 is the low RDSON trenched N-CH MOSFETs with robust ESD protection. This product is suitable for Lithium-ion one cell battery pack applications.

The FKBE2738 meet the RoHS and Green Product requirement with full function reliability approved.

## PRPAK3x3 NEP Pin Configuration



## Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	20	V
$V_{GS}$	Gate-Source Voltage	$\pm 12$	V
$I_D@T_A=25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 4.5\text{V}^1$	12	A
$I_D@T_A=70^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 4.5\text{V}^1$	9.6	A
$I_{DM}$	Pulsed Drain Current	72	A
$P_D@T_A=25^\circ\text{C}$	Total Power Dissipation <sup>3</sup>	1.32	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

## Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient <sup>1</sup> (Steady State)	---	95	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance Junction-ambient <sup>1</sup> ( $t < 10\text{S}$ )	---	55	$^\circ\text{C/W}$

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	20	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=4.5V, I_D=3A$	---	8.0	9.5	m $\Omega$
		$V_{GS}=4.0V, I_D=3A$	---	8.5	9.8	m $\Omega$
		$V_{GS}=3.1V, I_D=3A$	---	10.5	12.5	m $\Omega$
		$V_{GS}=2.5V, I_D=3A$	---	12.0	15.0	m $\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	0.5	---	1.5	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=16V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	$\mu A$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 12V, V_{DS}=0V$	---	---	$\pm 5$	$\mu A$
$g_{FS}$	Forward Transconductance	$V_{DS}=5V, I_D=6A$	---	28	---	S
$Q_g$	Total Gate Charge (4.5V)	$V_{DS}=15V, V_{GS}=4.5V,$ $I_D=10A$	---	13.5	---	nC
$Q_{gs}$	Gate-Source Charge		---	2.2	---	
$Q_{gd}$	Gate-Drain Charge		---	7.2	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=15V, V_{GS}=4.5V, R_G=6.0\Omega,$ $I_D=6A$	---	22	---	ns
$T_r$	Rise Time		---	85	---	
$T_{d(off)}$	Turn-Off Delay Time		---	125	---	
$T_f$	Fall Time		---	46	---	
$C_{iss}$	Input Capacitance	$V_{DS}=10V, V_{GS}=0V, F=1\text{MHz}$	---	775	---	pF
$C_{oss}$	Output Capacitance		---	255	---	
$C_{rss}$	Reverse Transfer Capacitance		---	230	---	

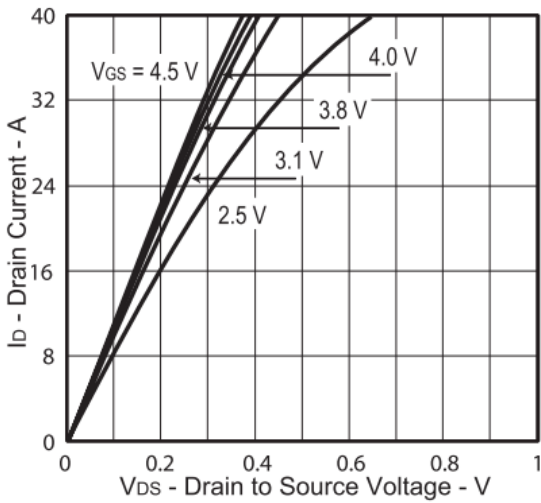
**Diode Characteristics**

Symbol	Parameter	Conditions	Max.	Unit
$I_S$	Continuous Source Current <sup>1,4</sup>	$V_G=V_D=0V, \text{Force Current}$	12	A
$I_{SM}$	Pulsed Source Current <sup>2,4</sup>		72	A
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	1.2	V

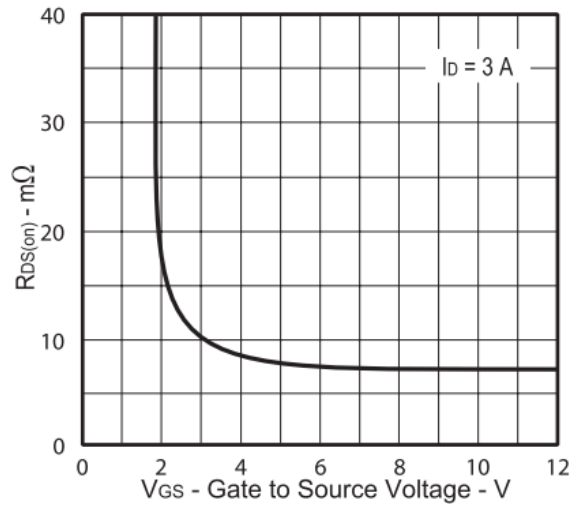
Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 20Z copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$
- 3.The power dissipation is limited by 150 $^\circ\text{C}$  junction temperature.
- 4.The data is theoretically the same as  $I_D$  and  $I_{DM}$  , in real applications , should be limited by total power dissipation.

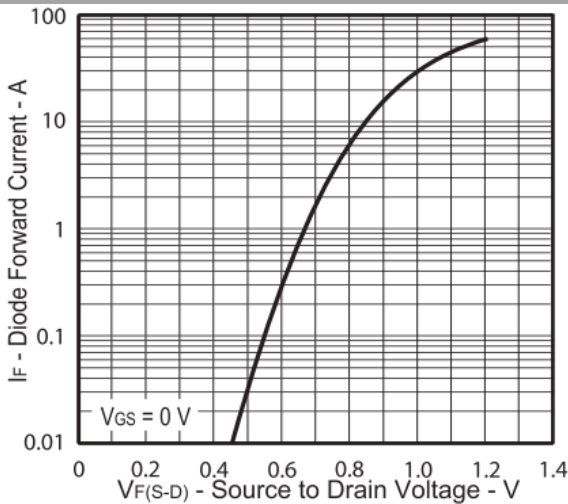
**Typical Characteristics**



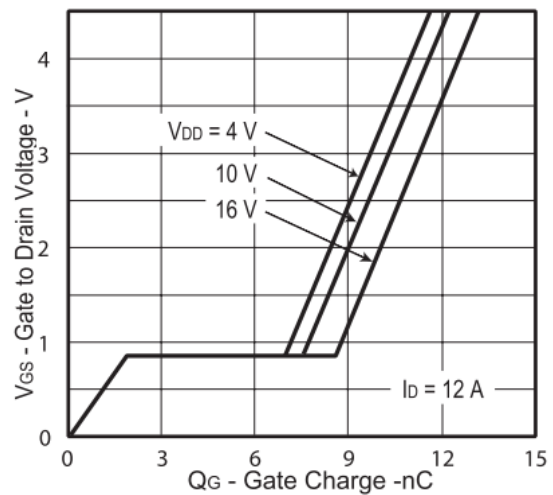
**Fig.1 Typical Output Characteristics**



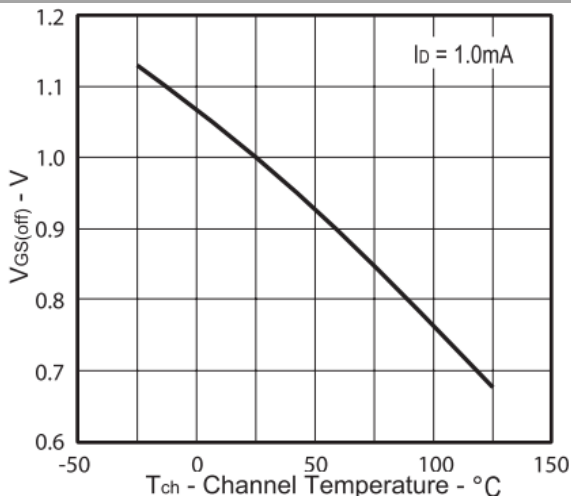
**Fig.2 On-Resistance vs. Gate-Source**



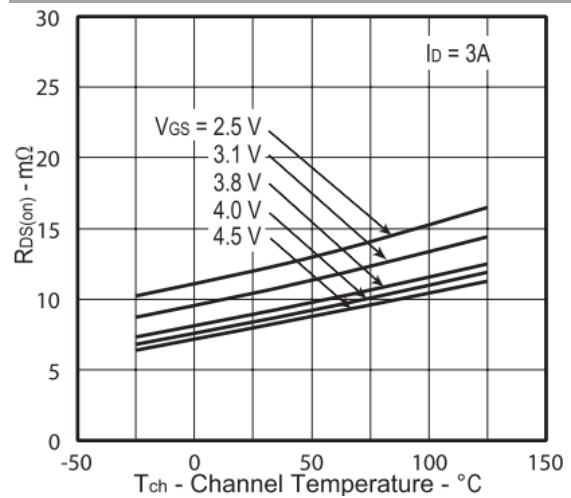
**Fig.3 Forward Characteristics Of Reverse**



**Fig.4 Gate-Charge Characteristics**

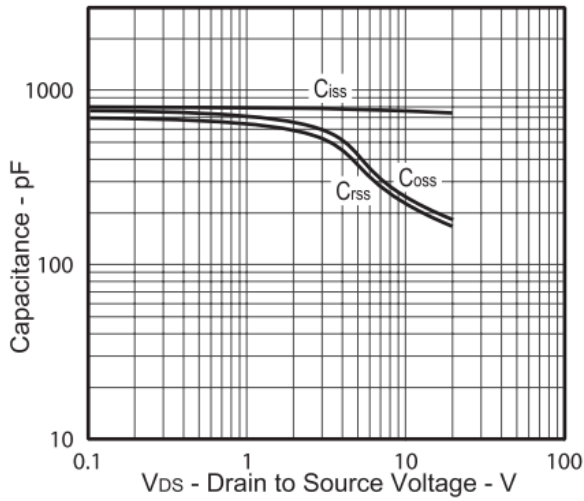


**Fig.5  $V_{GS(th)}$  vs.  $T_{CH}$**

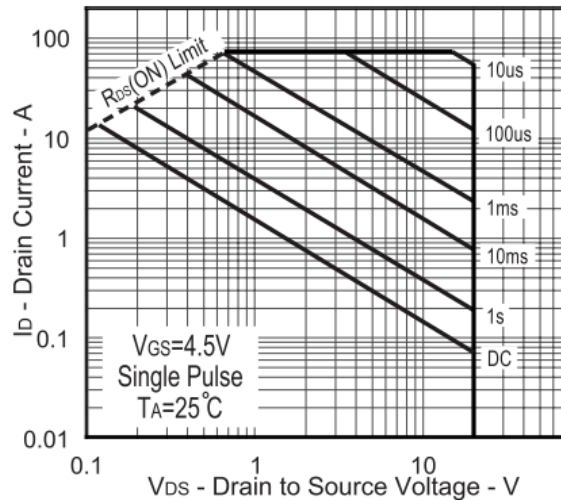


**Fig.6  $R_{DS(on)}$  vs.  $T_{CH}$**

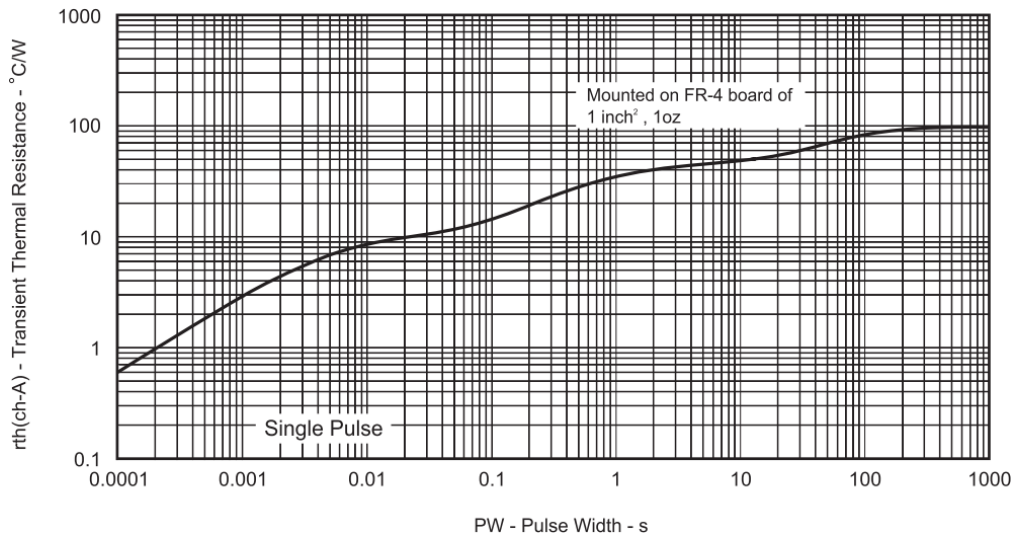
**Dual N-Ch 20V Fast Switching MOSFETs**



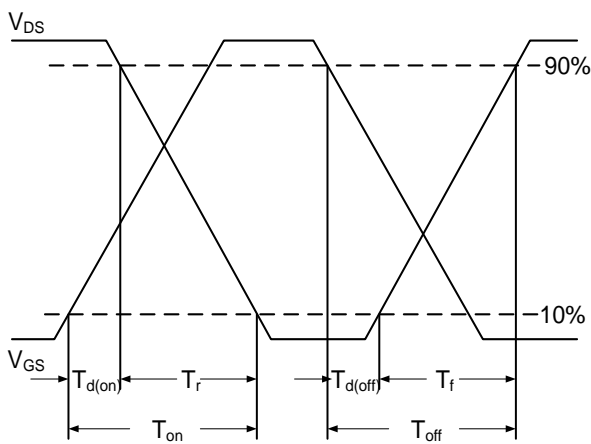
**Fig.7 Capacitance**



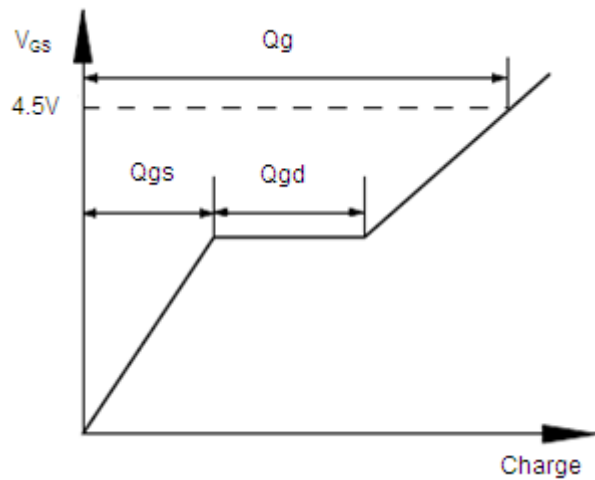
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**



**Fig.10 Switching Time Waveform**



**Fig.11 Gate Charge Waveform**