

2N7002K规格书 V1.0

N-Channel Trench Power MOSFET

ZLW-QW-EN-G131



台湾卓朗微科有限公司

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Product Summary

| $V_{(BR)DSS}$ | $R_{DS(on)MAX}$ | I_D |
|---------------|------------------|-------|
| 60V | $2\Omega@10V$ | 340mA |
| | $2.7\Omega@4.5V$ | |

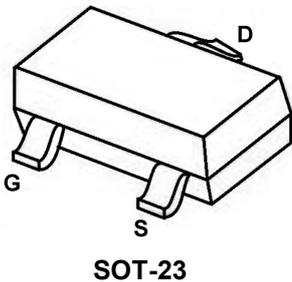
Feature

- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- Capable doing Cu wire bonding
- ESD protected Gate HBM 2KV

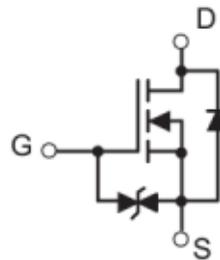
Application

- Power Management in Note book
- Portable Equipment
- Battery Powered System

Package:



Circuit diagram



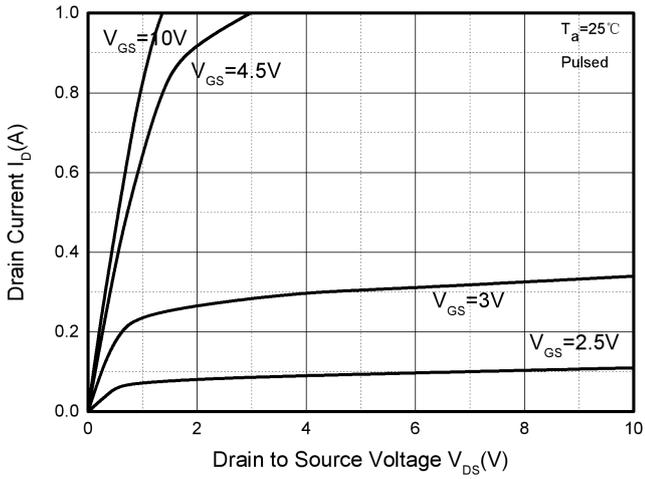
Absolute maximum ratings (Ta=25°C unless otherwise noted)

| Parameter | Symbol | Value | Unit |
|--------------------------|-----------|-----------|------|
| Drain-Source Voltage | V_{DS} | 60 | V |
| Gate-Source Voltage | V_{GS} | ± 20 | V |
| Continuous Drain Current | I_D | 340 | mA |
| Power Dissipation | P_D | 0.35 | W |
| Junction Temperature | T_J | 150 | °C |
| Storage Temperature | T_{STG} | -55~ +150 | °C |

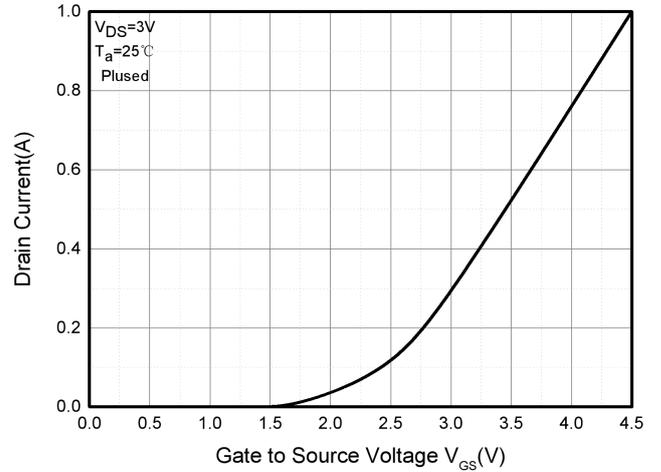
Electrical characteristics (Ta=25 °C, unless otherwise noted)

| Parameter | Symbol | Test Condition | Min. | Typ. | Max. | Unit |
|---|--------------|--|------|------|----------|----------|
| Static Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV_{DSS} | $V_{GS}=0V, I_D=250\mu A$ | 60 | | | V |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS}=V_{GS}, I_D=250\mu A$ | 1 | 1.5 | 2.5 | V |
| Gate-Body Leakage | I_{GSS} | $V_{DS}=0V, V_{GS}=\pm 20V$ | | | ± 10 | μA |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{DS}=60V, V_{GS}=0V$ | | | 1 | μA |
| Drain-Source On-Resistance | $R_{DS(ON)}$ | $V_{GS}=10V, I_D=500mA$ | | 1.1 | 2 | Ω |
| | | $V_{GS}=4.5V, I_D=200mA$ | | 1.2 | 2.7 | |
| Dynamic characteristics | | | | | | |
| Total Gate Charge | Q_g | $V_{DS}=10V, V_{GS}=4.5V, I_D=250mA$ | | 0.3 | | nC |
| Gate-Source Charge | Q_{gs} | | | 0.2 | | |
| Gate-Drain Charge | Q_{gd} | | | 0.08 | | |
| Input Capacitance | C_{iss} | $V_{DS}=25V, V_{GS}=0V, f=1MHz$ | | 30 | 50 | μF |
| Output Capacitance | C_{oss} | | | 4.2 | 25 | |
| Reverse Transfer Capacitance | C_{rss} | | | 2.9 | 5 | |
| Switching Characteristics | | | | | | |
| Turn-On Delay Time | $t_{d(on)}$ | $V_{DD}=30V, I_D=200mA, V_{GEN}=10V, R_G=25\Omega$ | | 3.9 | | ns |
| Turn-On Rise Time | t_R | | | 3.4 | | |
| Turn-Off Delay Time | $t_{d(off)}$ | | | 15.7 | | |
| Turn-Off Fall Time | t_F | | | 9.9 | | |
| Source-Drain Diode characteristics | | | | | | |
| Diode Forward Voltage | V_{SD} | $I_S=200mA, V_{GS}=0V$ | | 0.82 | 1.3 | V |

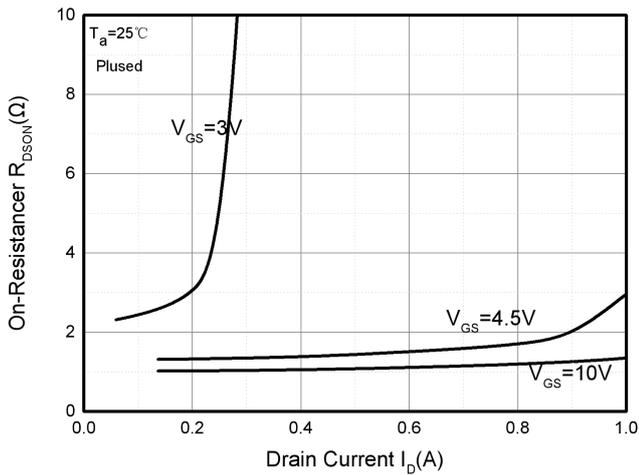
Typical Characteristics



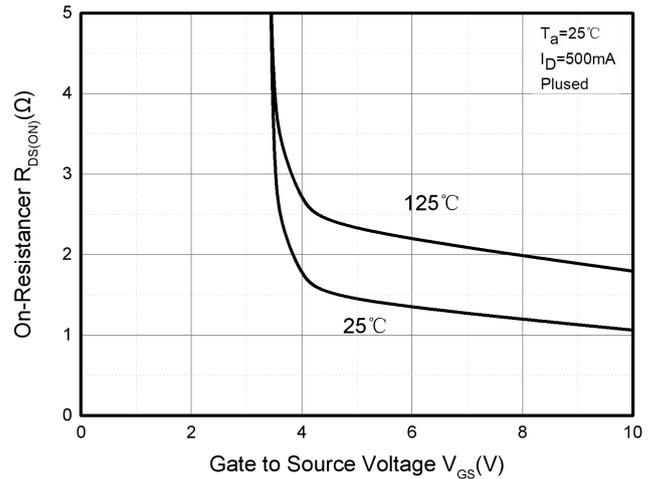
Output Characteristics



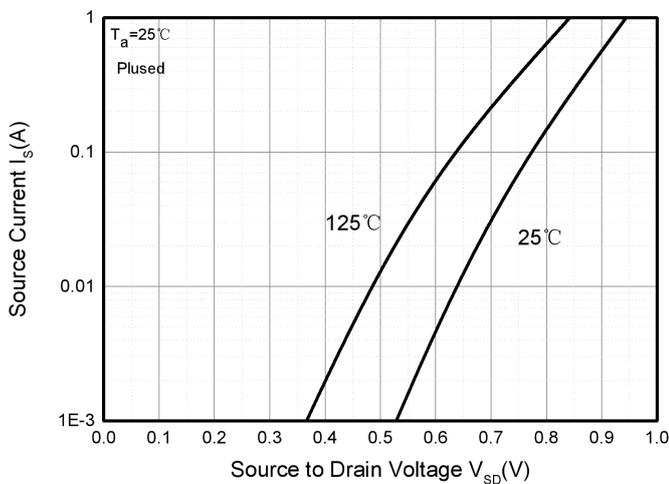
Transfer Characteristics



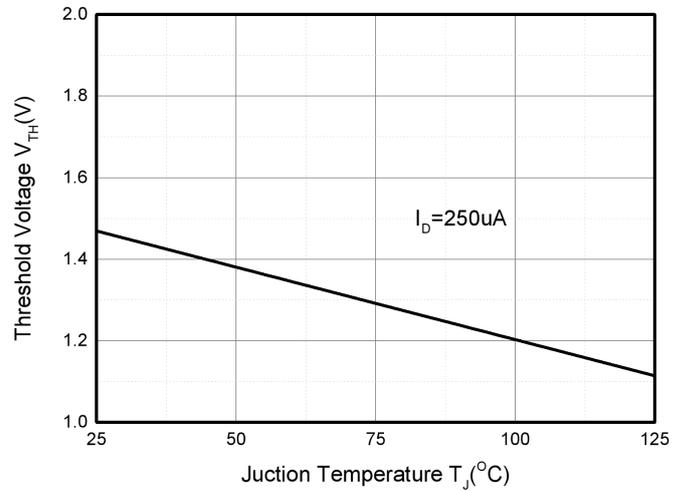
On-Resistance vs. Drain current



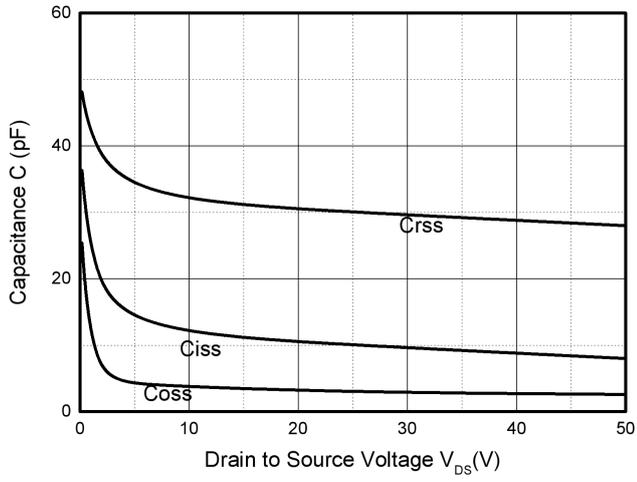
On-Resistance vs. Gate to Source Voltage



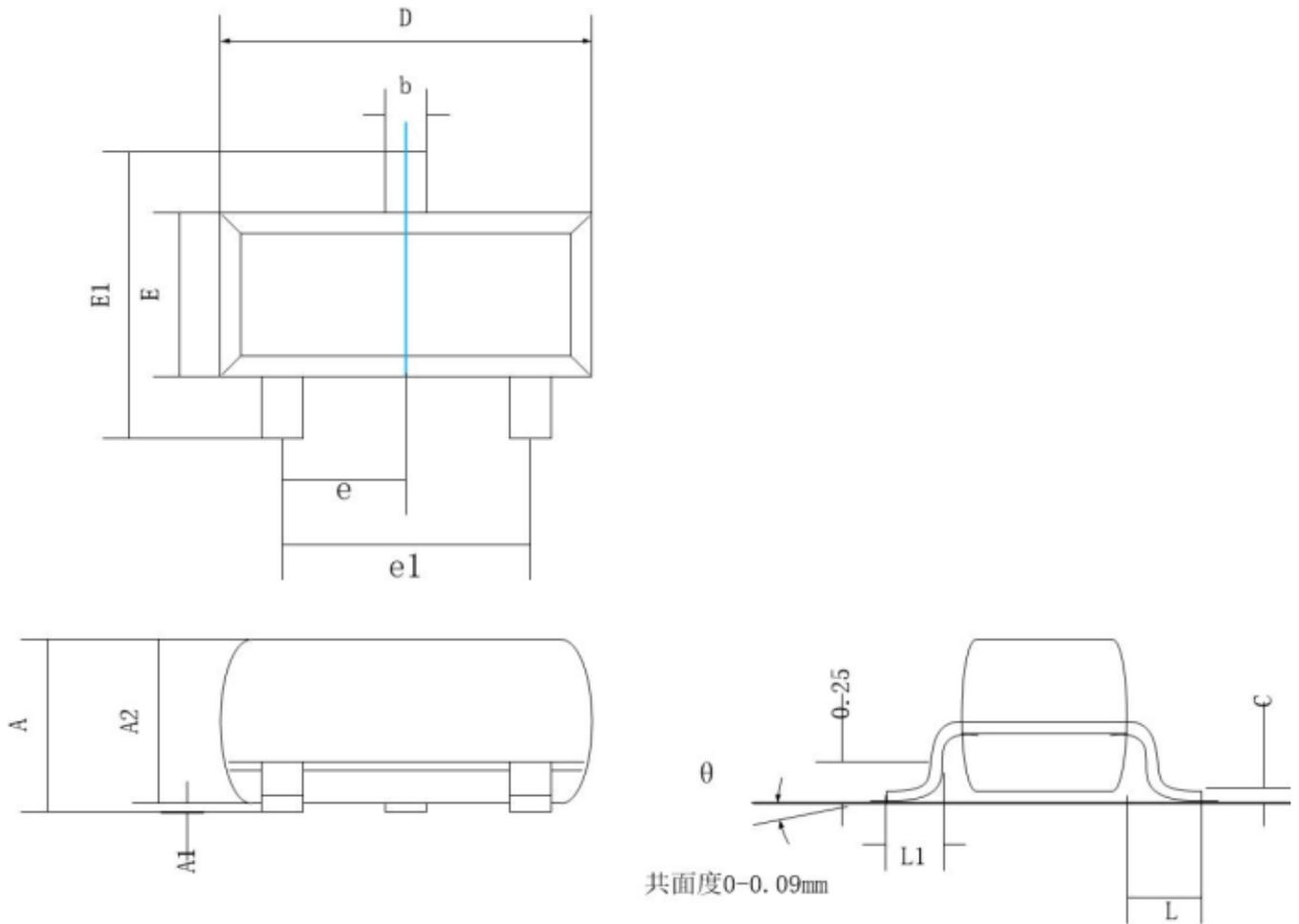
Source Current vs. Source to Drain Voltage



Threshold voltage vs. Junction temperature



Drain to Source Voltage vs. Capacitance

SOT-23 Package Information


| Symbol | Dimensions In Millimeters | |
|----------|---------------------------|------|
| | Min. | Max. |
| A | 0.90 | 1.15 |
| A1 | 0.00 | 0.10 |
| A2 | 0.90 | 1.05 |
| b | 0.30 | 0.50 |
| c | 0.08 | 0.15 |
| D | 2.80 | 3.00 |
| E | 1.20 | 1.40 |
| E1 | 2.25 | 2.55 |
| e | 0.95 REF. | |
| e1 | 1.80 | 2.00 |
| L | 0.55 REF. | |
| L1 | 0.30 | 0.50 |
| θ | 0° | 8° |